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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/776, 475				
Filing Date	2/10/2004				
First Named Inventor	Gunn III, et al				
Art Unit	Not Jet Assigned				
Examiner Name	4				
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
Pop	1	"LOW LOSS NANOPHOTONIC WAVEGUIDES AND RING May RESONATORS IN SILICON-ON-INSULATOR"; BEATS, ET AL 2004	
0099	2	"LARGE-SCALE PRODUCTION TECHNIQUES FOR PHOTONIC Agust NANOSTRUCTURES"; BOGAERTS, ET AL	
PP	3	"CMOS-COMPATIBLE PLANAR SILICON WAVEGUIDE-GRATING-COUPLER PHOTODETECTORS FABRICATED ON SILICON-ON-INSULATOR (SOI) SUBSTRATES"; S.M. CSUTAK, S. DAKSHINA-MURTHY &	
DIP		JOE C. CAMPBELL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.38 NO. 5 MAY 2002	
M	4.	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.28 NO. 1 JANUARY 1992.	
PAR	5	"INTEGRATED OPTICS FOR OPTICAL HEAD APPLICATIONS"; OCHIAI ET AL; ELECTRONICS LETTERS 18TH AUGUST 1994 VOL. 30 NO. 17, PAGE 1403-1404	
P99	6	"EFFECTS OF GRATING HEIGHTS ON HIGHLY EFFICIENT UNIBOND SOI WAVEGUIDE GRATING COUPLERS": ANG, ET AL; IEEE PHOTONICS TECHNOLOGY LETTERS VOL.12 NO. 1 JAN 2000	·
P93	7	"MODE MATCHING TECHNIQUE FOR HIGHLY EFFICIENT COUPLING BETWEEN DIELECTRIC WAVEGUIDES AND PLANAR PHOTONIC CRYSTAL CIRCUITS"; SANCHIS, ET AL Decenter 2002	
P93	8	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II; WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL 28 NO.1 JANUARY 1992	
Dap	9	"NON UNIFORM GRATING COUPLERS FOR COUPLING OF GAUSSIAN BEAMS TO COMPACT WAVEGUIDES": WEST, ET AL; PUBLISHED IN THE INTEGRATED PHOTONICS RESEARCH TECHNICAL DIGEST 1994	

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